

Electronic Acknowledgement Receipt

EFS ID:	2409230
Application Number:	10750150
International Application Number:	
Confirmation Number:	7903
Title of Invention:	Utilizing an advanced load address table for memory disambiguation in an out of order processor
First Named Inventor/Applicant Name:	Gary N. Hammond
Customer Number:	8791
Filer:	Joseph Lutz/Elaine Kwak
Filer Authorized By:	Joseph Lutz
Attorney Docket Number:	42P18219
Receipt Date:	31-OCT-2007
Filing Date:	31-DEC-2003
Time Stamp:	19:04:50
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 810
RAM confirmation Number	5210
Deposit Account	022666
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)				
1		42P18219_RFOA_100907.pdf	415992 fbc5fa82eb7844ae60dd4708468bd3d83cf6cbf7	yes	13				
	Multipart Description/PDF files in .zip description								
	Document Description		Start	End					
	Amendment After Final		1	1					
	Claims		2	7					
	Applicant Arguments/Remarks Made in an Amendment		8	13					
Warnings:									
Information:									
2	Request for Continued Examination (RCE)	42P18219_RCE_103107.pdf	48082 b17cbe79c698dd250a64e878b5c790826efe109	no	1				
Warnings:									
This is not a USPTO supplied RCE SB30 form.									
Information:									
3	Fee Worksheet (PTO-06)	fee-info.pdf	8208 091dc2a287b1743962f135fb18e57b623209ab84	no	2				
Warnings:									
Information:									
Total Files Size (in bytes):			472282						
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